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Erratum

Erratum regarding Previously Published Articles



Declaration of conflict of Interest statements were not included or had errors in the published versions of the following articles, that appeared in previous issues of BenchCouncil Transactions on Benchmarks, Standards and Evaluations.

The appropriate Declaration/Conflict of Interest statements, provided by the Authors, are included below.

- "Workflow Critical Path: A data-oriented critical path metric for Holistic HPC Workflows" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100001) https://doi.org/10.1016/j.tbench.2021.100001
 - Declaration of interest: The Authors have no interests to declare.
- "MLHarness: A scalable benchmarking system for MLCommons" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100002) https://doi.org/10.1016/j.tbench.2021.100002
 - Declaration of interest: The Authors have no interests to declare.
- "Performance optimization opportunities in the Android software stack" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100003) https://doi.org/ 10.1016/j.tbench.2021.100003
 - Declaration of interest: The Authors have no interests to declare.
- "Benchmarking feature selection methods with different prediction models on large-scale healthcare event data" (Bench-Council Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100004) https://doi.org/10.1016/j.tbench.2021. 100004
 - Declaration of interest: The Authors have no interests to declare.
- 5. "Comparative evaluation of deep learning workloads for leadership-class systems" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100005) https://doi.org/10.1016/j.tbench.2021.100005
 - Declaration of interest: The Authors have no interests to declare.

- "Benchmarking for Observability: The Case of Diagnosing Storage Failures" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100006) https://doi.org/10.1016/j.tbench.2021.100006
 - Declaration of interest: The Authors have no interests to declare.
- "A parallel sparse approximate inverse preconditioning algorithm based on MPI and CUDA" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100007) https://doi.org/10.1016/j.tbench.2021.100007
 - Declaration of interest: The Authors have no interests to declare.
- "MVDI25K: A large-scale dataset of microscopic vaginal discharge images" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100008) https://doi.org/10.1016/j.tbench.2021.100008
 - Declaration of interest: The Authors have no interests to declare.
- "Latency-aware automatic CNN channel pruning with GPU runtime analysis" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100009) https://doi.org/10.1016/j.tbench.2021.100009
 - Declaration of interest: The Authors have no interests to declare.
- "Fallout: Distributed systems testing as a service" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100010) https://doi.org/10.1016/j.tbench.2021.100 010
 - Declaration of interest: All the authors were employed by DataStax at the time of writing.
- "Revisiting the effects of the Spectre and Meltdown patches using the top-down microarchitectural method and purchasing power parity theory" (BenchCouncil Transactions on Benchmarks, Standards and Evaluations Journal, 2021: 100011) https: //doi.org/10.1016/j.tbench.2021.100011
 - Declaration of interest: The Authors have no interests to declare.